Step #1:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

b)

Graphical user interface

Description automatically generated with low confidence

Step #3:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

b)

Graphical user interface

Description automatically generated with medium confidence

Step #4:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

ADDI X2, XZR, #570

LSL X2, X2, #3

ADDI X2, X2, #7

ADDI X8, XZR, #843

LSL X8, X8, #12

ADDI X9, XZR, #965

LSL X9, X9, #2

ADDI X9, X9, #1

ADD X10, X8, X9

Loop3:

ADD X11, X11, X10

SUBI X2, X2, #1

CBNZ X2, Loop3

STUR X11, [X3, #16]

b)Graphical user interface

Description automatically generated with medium confidence

Step #5:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

ADDI X2, XZR, #570

LSL X2, X2, #3

ADDI X2, X2, #7

ADDI X8, XZR, #843

LSL X8, X8, #12

ADDI X9, XZR, #965

LSL X9, X9, #2

ADDI X9, X9, #1

ADD X10, X8, X9

Loop3:

ADD X11, X11, X10

SUBI X2, X2, #1

CBNZ X2, Loop3

STUR X11, [X3, #16]

AND X12, X11, X7

STUR X12, [X3, #24]

b)

Table

Description automatically generated with medium confidence

Step #6

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

ADDI X2, XZR, #570

LSL X2, X2, #3

ADDI X2, X2, #7

ADDI X8, XZR, #843

LSL X8, X8, #12

ADDI X9, XZR, #965

LSL X9, X9, #2

ADDI X9, X9, #1

ADD X10, X8, X9

Loop3:

ADD X11, X11, X10

SUBI X2, X2, #1

CBNZ X2, Loop3

STUR X11, [X3, #16]

AND X12, X11, X7

STUR X12, [X3, #24]

ORR X13, X11, X7

STUR X13, [X3, #32]

b)

A picture containing table

Description automatically generated

Step #7:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

ADDI X2, XZR, #570

LSL X2, X2, #3

ADDI X2, X2, #7

ADDI X8, XZR, #843

LSL X8, X8, #12

ADDI X9, XZR, #965

LSL X9, X9, #2

ADDI X9, X9, #1

ADD X10, X8, X9

Loop3:

ADD X11, X11, X10

SUBI X2, X2, #1

CBNZ X2, Loop3

STUR X11, [X3, #16]

AND X12, X11, X7

STUR X12, [X3, #24]

ORR X13, X11, X7

STUR X13, [X3, #32]

EOR X14, X11, X7

STUR X14, [X3, #40]

b)

Table

Description automatically generated with medium confidence

Step #8:

a)

ADDI X0, XZR, #222

ADDI X4, XZR, #771

LSL X4, X4, #4

ADDI X4, X4, #9

Loop1:

ADD X5, X5, X4

SUBI X0, X0, #1

CBNZ X0, Loop1

STUR X5, [X3, #0]

ADDI X1, XZR, #222

ADDI X6, XZR, #916

LSL X6, X6, #8

ADDI X6, X6, #71

Loop2:

ADD X7, X7, X6

SUBI X1, X1, #1

CBNZ X1, Loop2

STUR X7, [X3, #8]

ADDI X2, XZR, #570

LSL X2, X2, #3

ADDI X2, X2, #7

ADDI X8, XZR, #843

LSL X8, X8, #12

ADDI X9, XZR, #965

LSL X9, X9, #2

ADDI X9, X9, #1

ADD X10, X8, X9

Loop3:

ADD X11, X11, X10

SUBI X2, X2, #1

CBNZ X2, Loop3

STUR X11, [X3, #16]

AND X12, X11, X7

STUR X12, [X3, #24]

ORR X13, X11, X7

STUR X13, [X3, #32]

EOR X14, X11, X7

STUR X14, [X3, #40]

SUB X15, XZR, X11

SUBI X16, X15, #1

STUR X16, [X3, #48]

b)

Table

Description automatically generated